What is claimed is:

1. A method comprising:

basing a discrete frequency transformation on the number of subcarriers in a predetermined set of subcarriers, one or more subcarriers of the set assigned to modulate data and the remaining subcarriers of the set not assigned to modulate the data;

performing the discrete frequency transformation on the data to modulate the data; and

excluding from the transformation mathematical operations associated with the subcarriers not assigned to modulate the data.

2. The method of claim 1, wherein the excluding comprises:

excluding all of the subcarriers not assigned to modulate the data.

3. The method of claim 1, wherein the performing the discrete frequency transformation comprises:

performing orthogonal frequency division multiplexing modulation on the data.

4. The method of claim 1, wherein the performing comprises:

applying a weighting function during the discrete frequency transformation to perform symbol shaping.

5. The method of claim 1, wherein said one or more subcarriers are assigned to at least one of a user, an electrical device and a terminal.

6. The method of claim 1, further comprising:

using the modulated data to form an orthogonal frequency division multiplexing symbol.

7. The method of claim 1, further comprising:

using the transformation to generate symbols at a rate defined by a symbol generation interval;

basing the discrete frequency transformation on the symbol generation interval; and

using the discrete frequency transformation to generate discrete modulated values for an interval that exceeds the symbol generation interval to generate a cyclic extension.

8. The method of claim 7, further comprising:

transmitting each of the symbols during one of the intervals that exceeds the symbol generation interval.

9. The method of claim 1, further comprising:

selectively pre-rotating phases of said one or more subcarriers to generate a cyclic prefix.

10. The method of claim 1, wherein the mathematical operations comprise at least one of an accumulate operation and a multiplication operation.

11. A system comprising:

a device to generate data to be modulated; and

a transmitter to:

base a discrete frequency transformation on the number of subcarriers in a predetermined set of subcarriers, one or more subcarriers of the set of subcarriers assigned to modulate data and the remaining subcarriers of the set not assigned to modulate the data;

perform the discrete frequency transformation on the data to modulate the data; and

exclude from the transformation mathematical operations associated with the subcarriers not assigned to modulate the data.

12. The system of claim 11, wherein the transmitter excludes all of the subcarriers not assigned to modulate the data.

13. The system of claim 11, wherein the transmitter performs orthogonal frequency division multiplexing modulation on the data.

14. The system of claim 11, wherein the transmitter determines components of the discrete frequency transformation independently from each other.

15. The system of claim 11, wherein said one or more subcarriers are assigned to one of a user, an electrical device and a terminal.

16. The system of claim 11, wherein the transmitter uses the modulated data to form an orthogonal frequency division multiplexing symbol.

17. The system of claim 11, wherein the transmitter:

uses the transformation to generate symbols at a rate defined by a symbol generation interval;

bases the discrete frequency transformation on the symbol generation interval; and

uses the discrete frequency transformation to generate discrete modulated values for an interval that exceeds the symbol generation interval to generate a cyclic extension.

18. The system of claim 11, wherein the transmitter transmits each of the symbols during one of the intervals that exceeds the symbol generation interval.

19. The system of claim 11, wherein the transmitter selectively pre-rotates phases of said one or more subcarriers to generate a cyclic prefix.

20. The system of claim 11, wherein the mathematical operations comprise at least one of an accumulate operation and a multiplication operation.

21. An article comprising a storage medium readable by a processor-based system, the storage medium storing instructions to cause a processor to:

base a discrete frequency transformation on the number of subcarriers in a predetermined set of subcarriers, one or more subcarriers of the set assigned to modulate data and the remaining subcarriers not assigned to modulate the data;

perform the discrete frequency transformation on the data to modulate the data; and

exclude from the transformation mathematical operations associated with the subcarriers not assigned to modulate the data.

22. The article of claim 21, the storage medium storing instructions to cause the processor to exclude from the transformation all mathematical operations associated with the subcarriers not assigned to modulate the data.

23. The article of claim 21, the storage medium storing instructions to cause the processor to perform orthogonal frequency division multiplexing modulation on the data.

24. The article of claim 21, the storage medium storing instructions to cause the processor to determine components of the inverse discrete frequency transformation independently from each other.

25. The article of claim 21, wherein said one or more subcarriers are assigned to one of a user, an electrical device and a terminal.

26. The article of claim 21, the storage medium storing instructions to cause the processor to use the modulated data to form an orthogonal frequency division multiplexing symbol.

27. The article of claim 21, the storage medium storing instructions to cause the processor to:

use the transformation to generate symbols at a rate defined by a symbol generation interval;

base the discrete frequency transformation on the symbol generation interval; and

use the discrete frequency transformation to generate discrete modulated values for an interval that exceeds the symbol generation interval to generate a cyclic extension.

28. The article of claim 27, the storage medium storing instructions to cause the processor to:

transmit each of the symbols during one of the intervals that exceeds the symbol generation interval.

29. The article of claim 21, the storage medium storing instructions to cause the processor to:

selectively pre-rotate phases of said one or more subcarriers to generate a cyclic prefix.

30. The article of claim 21, wherein the mathematical operations comprise at least one of an accumulate operation and a multiplication operation.